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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	. CONFIRMATION NO.
10/042,444	01/08/2002	Guido Plangger	CH 010004	4621
24737	7590 12/02/20	04	EXAMINER	
PHILIPS IN	ITELLECTUAL PI	OSORIO, RICARDO		
P.O. BOX 30	001 F MANOR, NY 10	510	ART UNIT	PAPER NUMBER
BRITICELL	i minion, mi	510	2673	

DATE MAILED: 12/02/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/042,444	PLANGGER, GUIDO				
Office Action Summary	Examiner	Art Unit				
	RICARDO L OSORIO	2673	(			
The MAILING DATE of this communication Period for Reply	appears on the cover sheet wi	th the correspondence address				
A SHORTENED STATUTORY PERIOD FOR RITHE MAILING DATE OF THIS COMMUNICATION Extensions of time may be available under the provisions of 37 Clafter SIX (6) MONTHS from the mailing date of this communication. If the period for reply specified above, the maximum statutory properties of the period for reply in the set or extended period for reply within the set or extended peri	ON. FR 1.136(a). In no event, however, may a r in. a reply within the statutory minimum of thin eriod will apply and will expire SIX (6) MON statute, cause the application to become AE	eply be timely filed  y (30) days will be considered timely.  THS from the mailing date of this communication  ANDONED (35 U.S.C. § 133).	<b>1</b> .			
Status						
1) Responsive to communication(s) filed on	18 June 2004.					
	This action is non-final.					
• •	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) ⊠ Claim(s) 1-6 and 8-10 is/are pending in th 4a) Of the above claim(s) is/are with 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) 1-6 and 8-10 is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction a	ndrawn from consideration.					
Application Papers						
9)☐ The specification is objected to by the Exa	miner.					
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the co			d).			
Priority under 35 U.S.C. § 119						
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>						
Attachment(s)						
1) Notice of References Cited (PTO-892)	4) Interview S	Summary (PTO-413)				
Notice of Draftsperson's Patent Drawing Review (PTO-94:     Information Disclosure Statement(s) (PTO-1449 or PTO/S Paper No(s)/Mail Date		s)/Mail Date nformal Patent Application (PTO-152) 				

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## **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1-6 and 8-10 are rejected under 35 U.S.C. 102(b) as being anticipated by Hayashi (5,994,916).

Regarding claims 1, 5 and 10, Hayashi teaches of a display device driver provided with leads (Fig. 11, reference characters 23(1)-23(p)) coupled to at least one multiplexer (Fig. 1, reference character 47) and to a first switching device (Fig. 1, reference character 40) that enables interruption of a voltage supply from a voltage generator (Fig. 1, reference character 39) to the leads (col. 6, lines 60-67), and also provided with at least one second switching device (Fig. 1, reference character SW) that is coupled to the leads and whereby at least one of the leads can be switched to a selectable potential (col. 7, lines 41-50), wherein the second switching device in the output stage switches the lead that is selected by the multiplexer (col. 6, lines 51-57) to a test reference potential (col. 7, lines 41-50).

Regarding claim 2, Hayashi further teaches of the output stages being provided with at least one multiplexer (Fig. 1, reference character 47 and at least one amplifier (Fig. 1, reference character 35).

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Regarding claim 3, Hayashi further teaches that second switches devices are provided in all output stages (Fig. 1, reference character SW. All output stages can have their own second switch or multiplexer).

Regarding claim 4, Hayashi teaches that the multiplexer can be controlled by a digital signal arranged to switch a voltage present on the leads (col. 6, lines 51-54).

Regarding claim 6, Hayashi further teaches that in a test mode the first switch connects the leads to a common potential and separates them from this potential (col. 6, lines 62-67).

Regarding claim 8, Hayashi further teaches of the switches being controlled separately (The switches are being controlled separately (Fig. 1, reference characters 39, 40, 46 and SW).

Regarding claim 9, Hayashi teaches that the output stages are connected to terminals of a display device (col. 6, lines 48-59).

## Response to Arguments

- 3. Applicant's arguments with respect to claims 1 and 10 have been considered but are moot in view of the new ground(s) of rejection.
- 4. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE

MONTHS from the mailing date of this action. In the event a first reply is filed within TWO

MONTHS of the mailing date of this final action and the advisory action is not mailed until after

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the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ricardo L. Osorio whose telephone number is 703 305-2248. The examiner can normally be reached on Monday through Thursday from 7:00 A.M. to 5:30 P.M. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala whose telephone number is 703 305-4938.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

or faxed to:

703 872-9306 (for Technology Center 2600 only)

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR

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system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Ricardo L. Osorio Examiner Art Unit: 2673

RLO November 17, 2004

> BIPIN SHALWALA SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2600